Lecture 25: Parallel Algorithms II

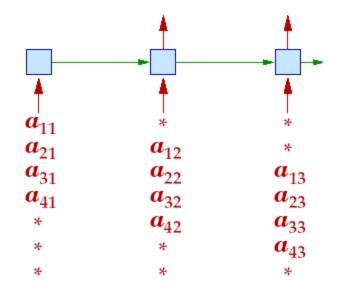
- Topics: matrix, graph, and sort algorithms
- Tuesday presentations:
 - Each group: 10 minutes
 - Describe the problem, your proposed solution, clarify novelty and related work, plan to get results, some early numbers

- Solving for x, where Ax=b and A is a nonsingular matrix
- Note that A⁻¹Ax = A⁻¹b = x ; keep applying transformations to A such that A becomes I ; the same transformations applied to b will result in the solution for x
- Sequential algorithm steps:
 - Pick a row where the first (ith) element is non-zero and normalize the row so that the first (ith) element is 1
 - Subtract a multiple of this row from all other rows so that their first (ith) element is zero
 - Repeat for all i

Sequential Example

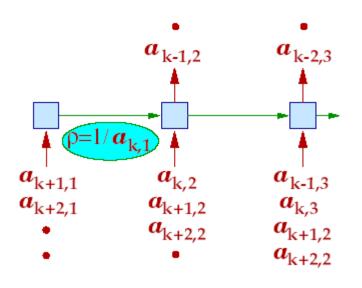
2	4	-7	x1	3	$\begin{array}{cccccccccccccccccccccccccccccccccccc$
3	6	-10	x2 =	4	
-1	3	-4	x3	6	
1 0 0	0	-7/2 1/2 -15/2	x1 x2 = x3	3/2 -1/2 15/2	$\begin{array}{rrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrr$
1	2	-7/2	x1	3/2	$\begin{array}{cccccccccccccccccccccccccccccccccccc$
0	1	-3/2	x2 =	3/2	
0	0	1/2	x3	-1/2	
1	0	-1/2	x1	-3/2	$ \begin{array}{rrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrr$
0	1	-3/2	x2 =	3/2	
0	0	1	x3	-1	

Algorithm Implementation

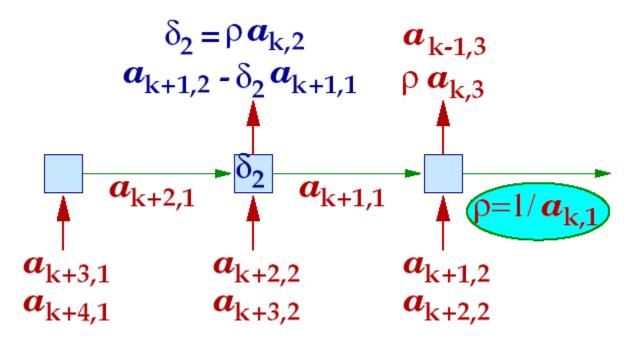


- The matrix is input in staggered form
- The first cell discards inputs until it finds a non-zero element (the pivot row)

- The inverse ρ of the non-zero element is now sent rightward
- ρ arrives at each cell at the same time as the corresponding element of the pivot row



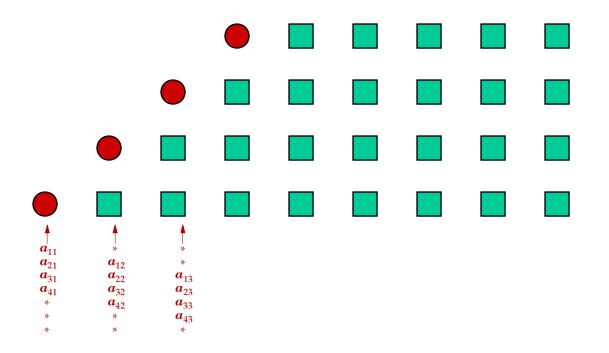
Algorithm Implementation



- Each cell stores $\delta_i = \rho a_{k,l}$ the value for the normalized pivot row
- This value is used when subtracting a multiple of the pivot row from other rows
- What is the multiple? It is a_{j,1}
- How does each cell receive a_{i,1}? It is passed rightward by the first cell
- Each cell now outputs the new values for each row
- The first cell only outputs zeroes and these outputs are no longer needed

Algorithm Implementation

- The outputs of all but the first cell must now go through the remaining algorithm steps
- A triangular matrix of processors efficiently implements the flow of data
- Number of time steps?
- Can be extended to compute the inverse of a matrix

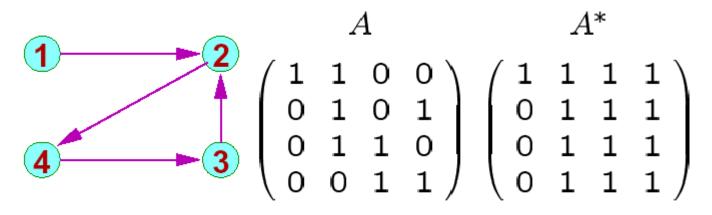


G = (V, E): a directed graph, $V = \{1, ..., N\}$ The *adjacency matrix* $A = (a_{ij})$ of G is

$$a_{ij} = \begin{cases} 1 & \text{if either } (i,j) \in E \text{ or } i = j, \\ 0 & \text{otherwise.} \end{cases}$$

The transitive closure of G is $G^* = (V, E^*)$,

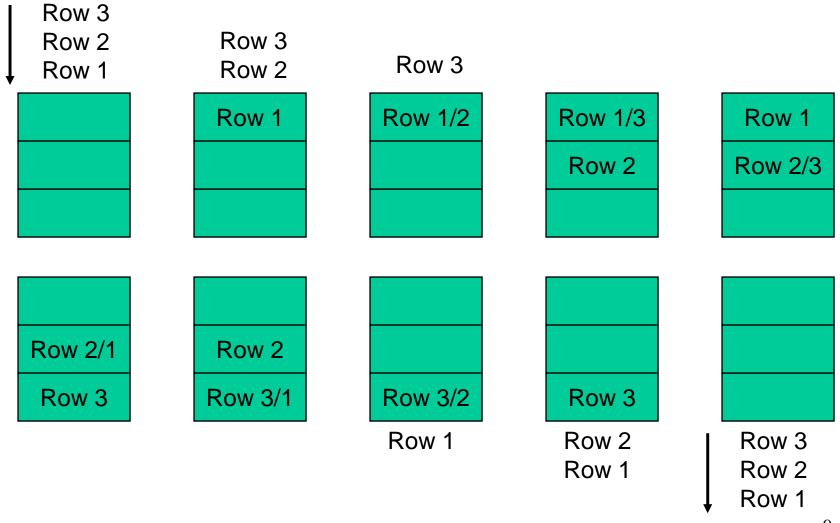
 $E^* = \{(i,j) \mid j \text{ is reachable from } i \text{ in } G\}.$



 $A^{(k)} =_{def} (a_{ij}^{(k)})$, where for each $k, 0 \le k \le N$, $a_{ij}^{(k)} = 1$ if j is reachable from i passing through only nodes $\le k$ and 0 otherwise.

Then $A^{(N)} = A^*$, $A^{(0)} = A$, and for all $k \ge 1$, $a_{ij}^{(k)} = a_{ij}^{(k-1)} \lor \left(a_{ik}^{(k-1)} \land a_{kj}^{(k-1)}\right)$.

Implementation on 2d Processor Array



- Diagonal elements of the processor array can broadcast to the entire row in one time step (if this assumption is not made, inputs will have to be staggered)
- A row sifts down until it finds an empty row it sifts down again after all other rows have passed over it
- When a row passes over the 1st row, the value of a_{i1} is broadcast to the entire row $-a_{ij}$ is set to 1 if $a_{i1} = a_{1j} = 1$ - in other words, the row is now the ith row of A⁽¹⁾
- By the time the kth row finds its empty slot, it has already become the kth row of A^(k-1)

 When the ith row starts moving again, it travels over rows a_k (k > i) and gets updated depending on whether there is a path from i to j via vertices < k (and including k)

- Given a graph and edges with weights, compute the weight of the shortest path between pairs of vertices
- Can the transitive closure algorithm be applied here?

 $D = (d_{ij})$: the distance matrix, where $d_{ij} = \infty$ if there is no edge from i to jDefine $D^{(k)} = (d_{ij}^{(k)})$, where $d_{ij}^{(k)}$ is the length of the shortest path from i to j that passes through only nodes $\leq k$.

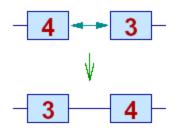
Then we have only to compute $D^{(N)}$. Note $D^{(0)} = D$ and for all $k \ge 1$,

$$d_{ij}^{(k)} = \min(d_{ij}^{(k-1)}, d_{ik}^{(k-1)} + d_{kj}^{(k-1)}).$$

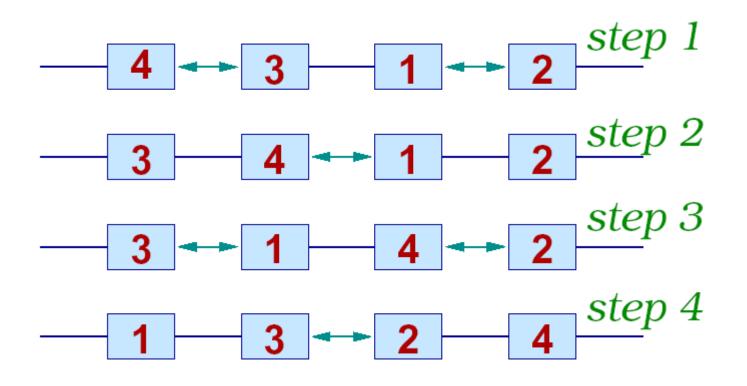
The above equation is very similar to that in transitive closure

Sorting with Comparison Exchange

- Earlier sort implementations assumed processors that could compare inputs and local storage, and generate an output in a single time step
- The next algorithm assumes comparison-exchange processors: two neighboring processors I and J (I < J) show their numbers to each other and I keeps the smaller number and J the larger



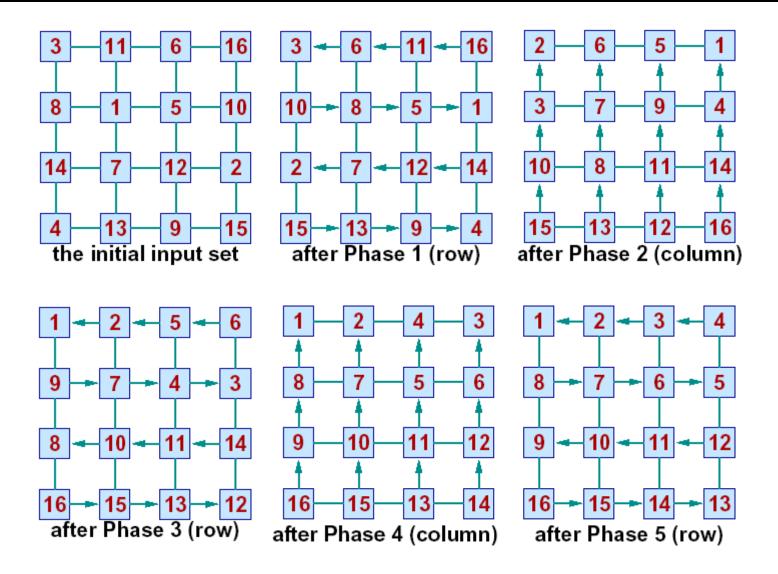
 N numbers can be sorted on an N-cell linear array in O(N) time: the processors alternate operations with their neighbors





- A sorting algorithm on an N-cell square matrix that improves execution time to O(sqrt(N) logN)
- Algorithm steps:
 - Odd phase: sort each row with odd-even sort (all odd rows are sorted left to right and all even rows are sorted right to left) Even phase: sort each column with odd-even sort Repeat
- Each odd and even phase takes O(sqrt(N)) steps the input is guaranteed to be sorted in O(logN) steps

Example



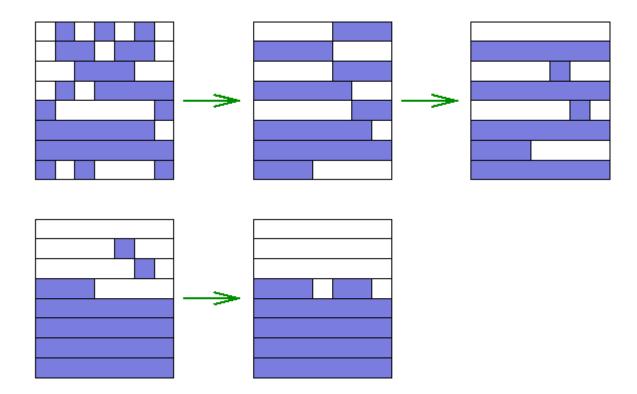
If a comparison-exchange algorithm sorts input sets consisting solely of 0's and 1's, then it sorts all input sets of arbitrary values

Proof Let an o.c.e. algorithm \mathcal{A} for input size N be given. Suppose that \mathcal{A} works on all 0-1 inputs. Assume that \mathcal{A} fails on an input $[x_1, \ldots, x_N]$ with $[y_1, \ldots, y_N]$ be the output. Then $y_1 \leq \cdots \leq y_m > y_{m+1}$ for some m. Define mapping F by F(x) = 0if $x < y_m$ and 1 otherwise. Since F preserves the order \leq , the output of \mathcal{A} on $[F(x_1), \ldots, F(x_N)]$ is $[F(y_1), \ldots, F(y_N)]$, and is of the form $[\ldots, 1, 0, \ldots]$ because of $y_m >$ y_{m+1} . This is a contradiction.

- How do we prove that the algorithm completes in O(logN) phases? (each phase takes O(sqrt(N)) steps)
- Assume input set of 0s and 1s
- There are three types of rows: all 0s, all 1s, and mixed entries – we will show that after every phase, the number of mixed entry rows reduces by half
- The column sort phase is broken into the smaller steps below: move 0 rows to the top and 1 rows to the bottom; the mixed rows are paired up and sorted within pairs; repeat these small steps until the column is sorted



• The modified algorithm will behave as shown below: white depicts 0s and blue depicts 1s



Proof

 If there are N mixed rows, we are guaranteed to have fewer than N/2 mixed rows after the first step of the column sort (subsequent steps of the column sort may not produce fewer mixed rows as the rows are not sorted)

Each pair of mixed rows produces at least one pure row when sorted

